



JFW

PATENT  
Docket No. INTEL/17479

**IN THE UNITED STATES PATENT  
AND TRADEMARK OFFICE**

**Applicant:**

Kiran R. DESAI

U.S. Serial No.: 10/630,164

For: "Methods and Apparatus for  
Maintaining Cache  
Coherency"

Filed: July 30, 2003

Assignee: Intel Corporation

Group Art Unit: 2186

**Examiner: Not Yet Assigned**

I hereby certify that this paper and the documents referred to as enclosed therewith are being deposited with the United States Postal Service as first class mail, postage prepaid, in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450 on this date:

**October 21, 2004**

  
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**James A. Flight**  
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## **STATUS LETTER**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Kindly advise when an Office action can be expected in the above-referenced matter.

Respectfully submitted,

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October 21, 2004

By:

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